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implementing packet classification in hardware

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P Gupta - 2000 - Citeseer

... The sequence of accesses made by the example **packet** have also been shown ... 148 Figure 4.30 An example **hardware** design for RFC with three phases. ... The leaf nodes store a maximum of birth **classification** rules.....163 Figure ...[Cited by 95](#) - [Related articles](#) - [View as HTML](#) - [All 16 versions](#)Multi-Engine **Packet Classification** Hardware AcceleratorA Kennedy, Z Liu, X Wang, B Liu - [doi.ieeecomputersociety.org](#)... 5 when only 1 **classification** engine is used. The sorter logic block and multiplexers used for multiplexing the output signals of the **classification** engines are not needed. IV. FPGA IMPLEMENTATION We **implement** the multi-engine **packet** classifier using VHDL on a Stratix ...[Related articles](#)[PDF] ► Routing lookups and **packet classification**: theory and practiceP Gupta - HOT INTERCONNECTS - [eeweb1.poly.edu](#)... packets into flows (**packet classification**) Page 11. 11 Routing Lookups ... Harder than Exact Match • The destination address of an arriving **packet** does not carry with it the information to determine the ... Cache management overhead > **Hardware** implementation difficult Page 40. 40 ...[Cited by 8](#) - [Related articles](#) - [View as HTML](#)[PDF] ► On using content addressable memory for **packet classification**

DE Taylor, EW Spitznagel - Dept. Comp. Sci. Eng., Washington Univ., Saint ..., 2005 - Citeseer

... The seminal Parallel Bit-Vectors (BV) scheme used a simple bit-vector intersection technique that maps well to **hardware** [5]. The presorting of filters by priority and poor ... The result is a high-performance **packet classification** technique that is practical to **implement** that has ...[Cited by 8](#) - [Related articles](#) - [View as HTML](#) - [All 3 versions](#)

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V Sahasranaman, M Buddhikot - Proceedings of IEEE International ..., 2001 - bell-labs.com
... 2.2 **Packet Classification** using Recursive Space-decomposition (PACARS) ... algorithm, we implemented search lists at each node as skiplists [6] and did not **implement** the fractional ... this set of potential tuples by hashing bits of the source and destination address of the **packet**. ...
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... To **implement** bit weaving, we must solve several chal- lenging technical problems. ... TCAM-based **packet classification** systems have been widely deployed due to their O(1) **classification** time ... array for a possibly incomplete rule list (ie, there may exist at least one **packet** that none ...
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... **Hardware** algorithms: This class of algorithms is implemented in dedicated **hardware** devices attacking PCP by massive parallelism ... match is reduced to at most 2k-i because each part of the **mask** match, corresponding ... The **packet classification** problem can be defined as follows ...
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